

Attorney Docket No. 0756-7227

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 10/733,352

Filed: December 12, 2003

For: PROCESS FOR PRODUCING A  
PHOTOELECTRIC CONVERSION  
DEVICE

) Group Art Unit: 2812

) Examiner: S. Isaac

) CERTIFICATE OF MAILING

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Adelle M. Stamps

**INFORMATION DISCLOSURE STATEMENT**

Honorable Commissioner of Patents  
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Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Copies of U.S. patents and U.S. publications are not enclosed in accordance with the Notice published in the Official Gazette on August 5, 2003 entitled *Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003*, which waives the requirement under 37 CFR 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. publication.

A check in the amount of \$180 is being submitted to comply with the provisions of 37 C.F.R. § 1.97(c).

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be

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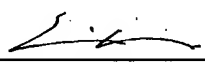
01 FC:1806

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Respectfully submitted,



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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)		Application Number	10/733,352
		Filing Date	December 12, 2003
		First Named Inventor	Shunpei YAMAZAKI et al.
		Group Art Unit	2812
		Examiner Name	S. Isaac
Sheet 1 of 4	Attorney Docket Number	0756-7227	

U.S. PATENT DOCUMENTS						
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
		2001/0034088		Nakamura et al.	10/25/2001	
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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
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		JP	2000-105081			04/11/2000		Abst.

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
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		L.S. LEE et al., <i>Argon Ion-Implantation on Polysilicon or Amorphous-Silicon for Boron Penetration Suppression in p+ pMOSFET</i> , IEEE Transactions on Electron Devices, Vol. 45, No. 8, August, 1998, Pages 1737-1744.	

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
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		Specification, claims, abstract of Application Serial No. 08/784,293 filed January 16, 1997 to Yamazaki et al.	

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